

#2 2122

**CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.5)**

Applicant(s): Allen et al.

Docket No.

BUR920010150US1

Serial No.

10/064,098

Filing Date

June 11, 2002

Examiner

Unkn wh

01/09/03  
Group Art Unit

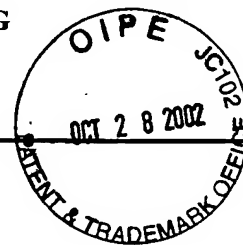
2122

Invention: **METHOD FOR IMPROVING CHIP YIELDS IN THE PRESENCE OF VIA FLARING**

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OCT 30 2002

Technology Center 2100



I hereby certify that this Submission of Formal Drawings

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**Frederick W. Gibb, III**

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#2  
01/09/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Allen et al.

Serial No.: 10/064,098

Group Art Unit: 2122

Filed: June 11, 2002

Examiner: Unknown

For: METHOD FOR IMPROVING CHIP YIELDS IN THE PRESENCE OF VIA FLARING

Assistant Commissioner of Patents  
Washington, D.C. 20231

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**SUBMISSION OF FORMAL DRAWINGS**

Technology Center 2100

Sir:

Submitted herewith are eight (8) sheets of formal drawings comprising Figs. 1A-8 for the above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please substitute these formal drawings for the drawings which were filed with the application.

Respectfully Submitted,

Frederick W. Gibbs, III

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Date: 10/22/02

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